


# ASTRIAL

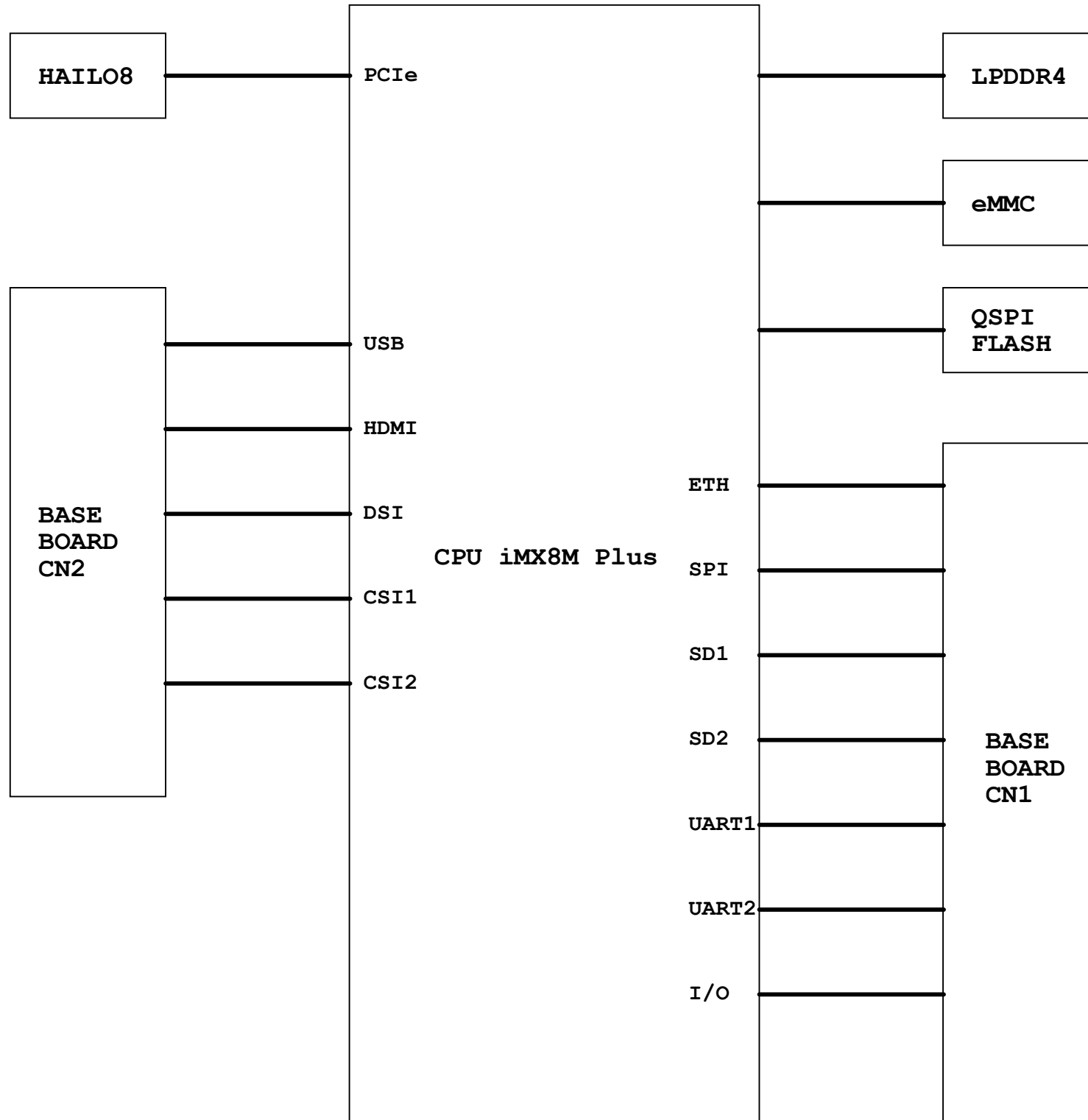
The following schematic of Astrial is the reduced version.  
The full version will be released only to customers upon explicit request to EBV Elektronik or System Electronics.

 a coesia brand	System Electronics Via Ghiarola Vecchia, 73 41042 Fiorano Modenese (MO)		
	<b>ASTRIAL v.1.0</b>		
Size A3	DWG NO PCB03	Rev 3.0	
09/09/2024	Scale	Sheet 1 of 17	

**POWER SUPPLY:**

- 5Vcc from Base Board
- 5Vcc..16Vcc from VEXT

HAILO8 needs an external 5V to be properly supplied



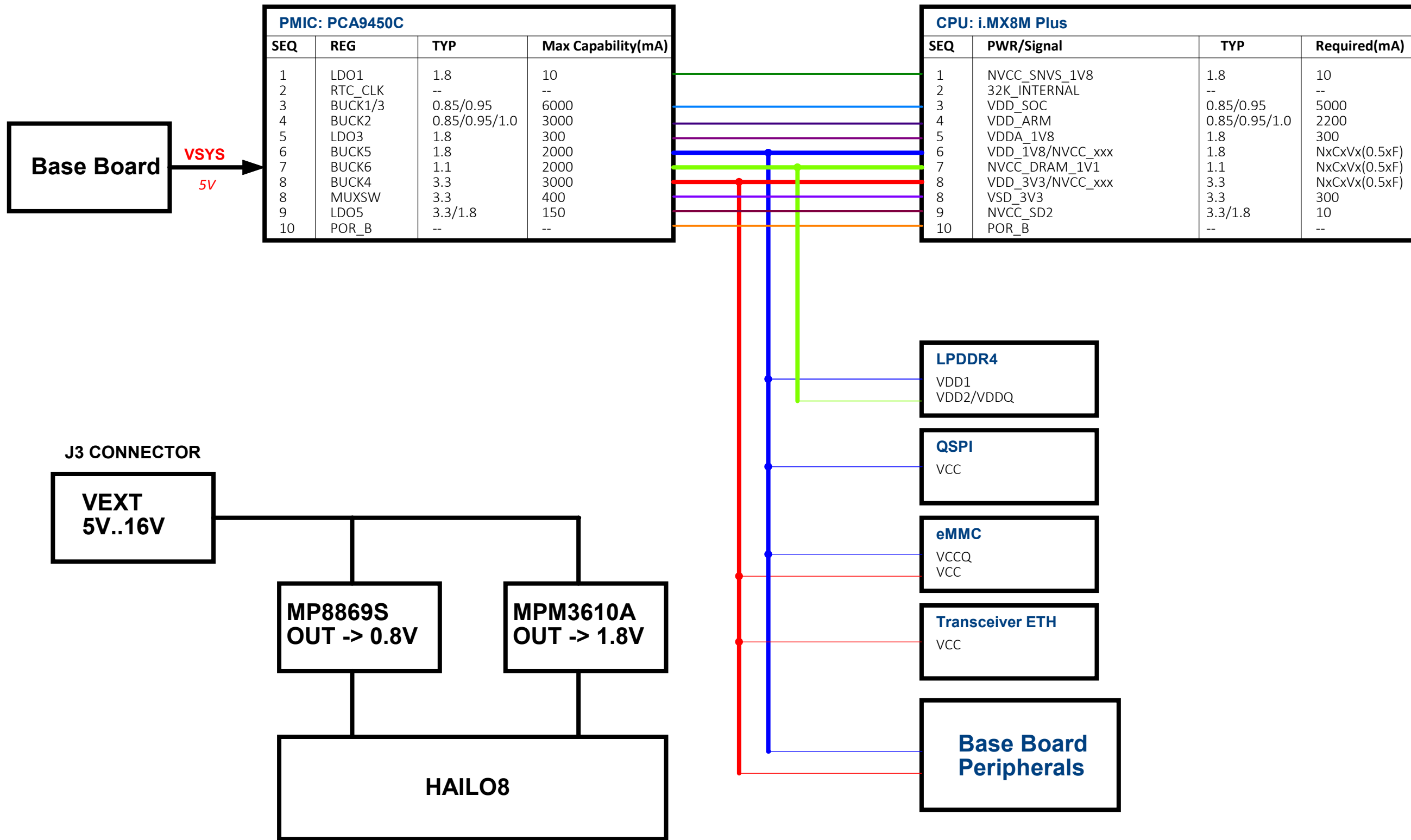
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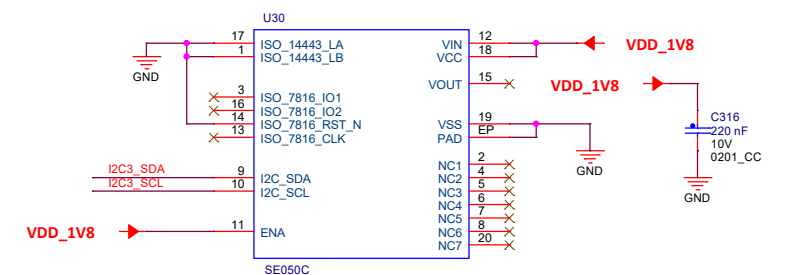
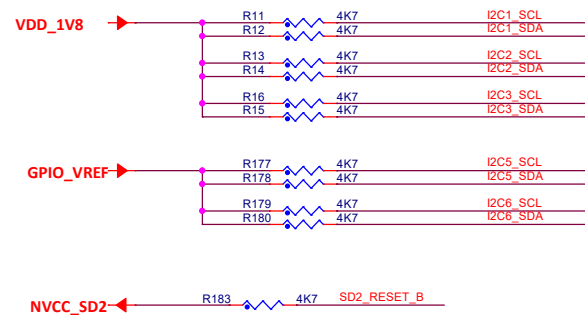
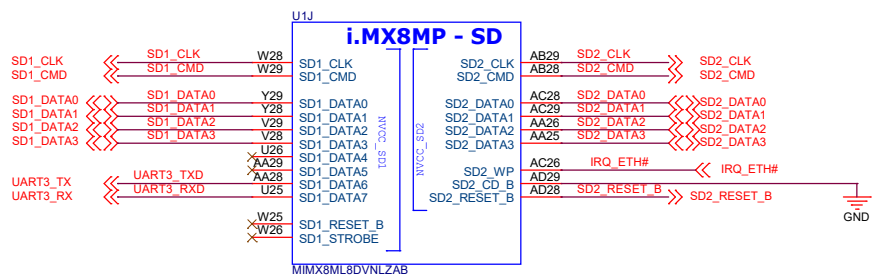
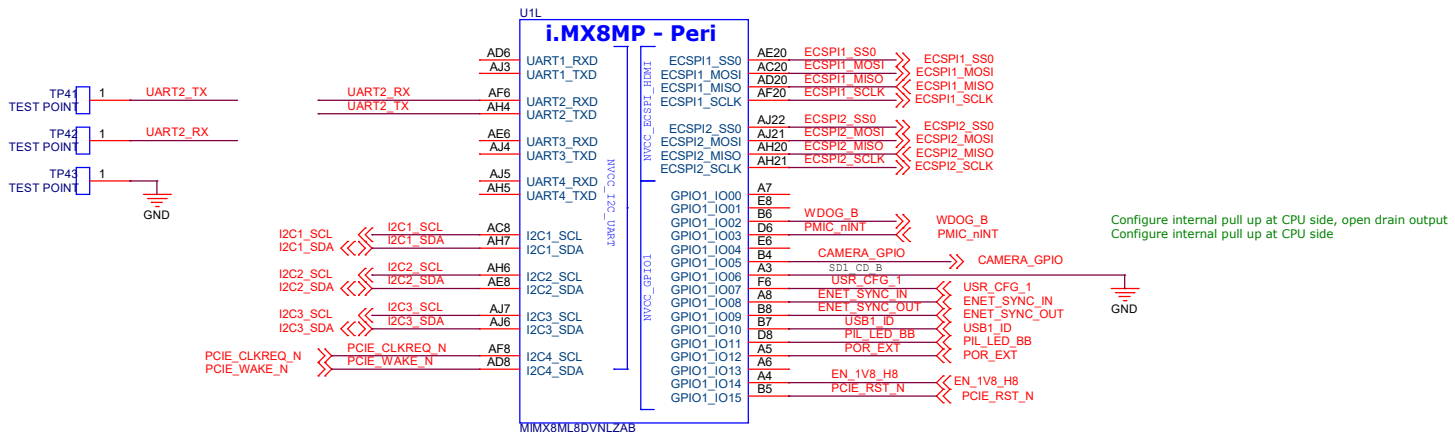
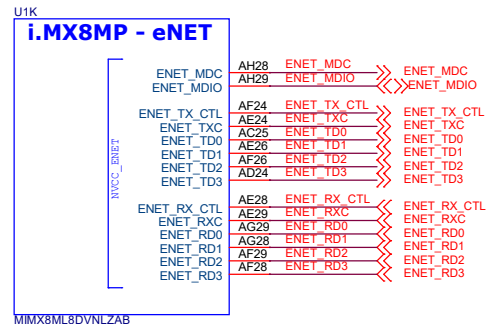
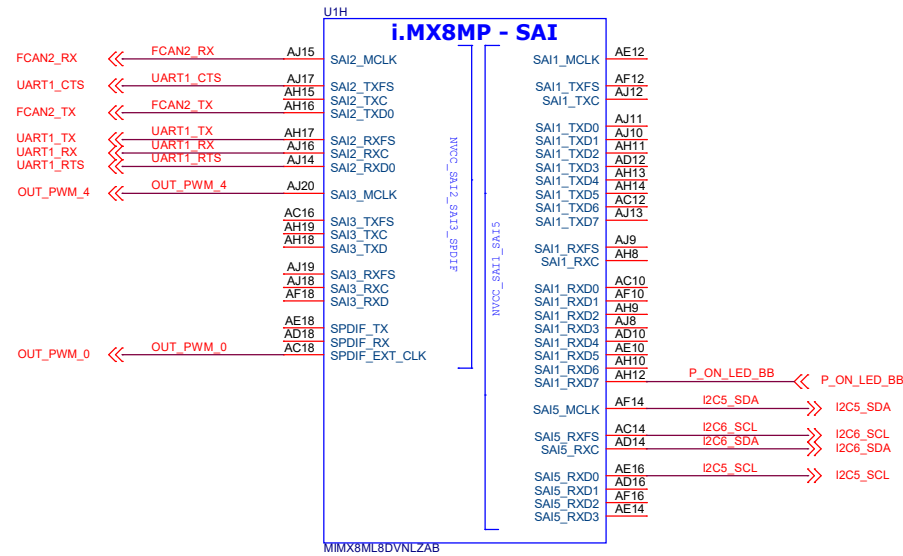
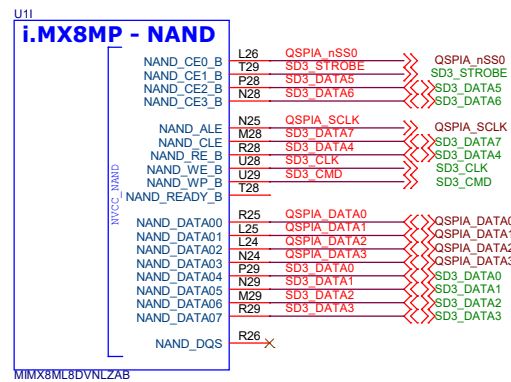
Size A3	DWG NO PCB03	Rev 3.0
Scale	Sheet 2 of 17	

09/09/2024

# ASTRIAL POWER TREE



# i.MX8M Plus IO Interface



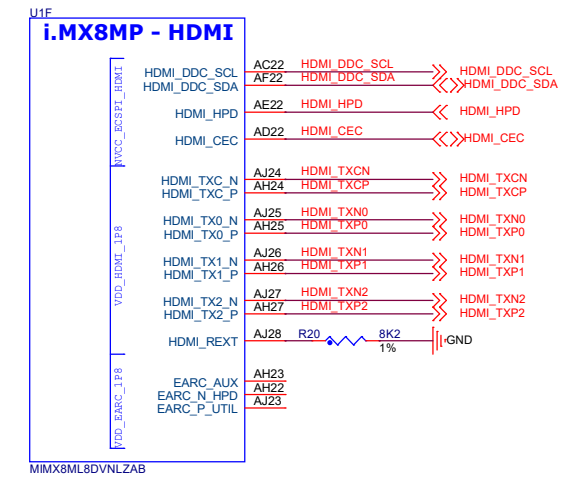
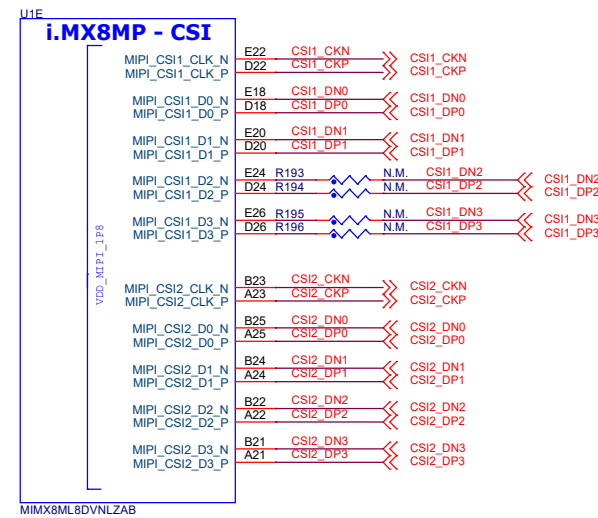
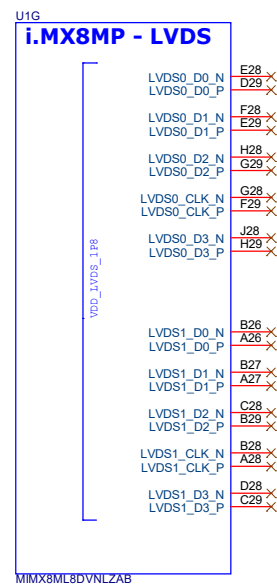
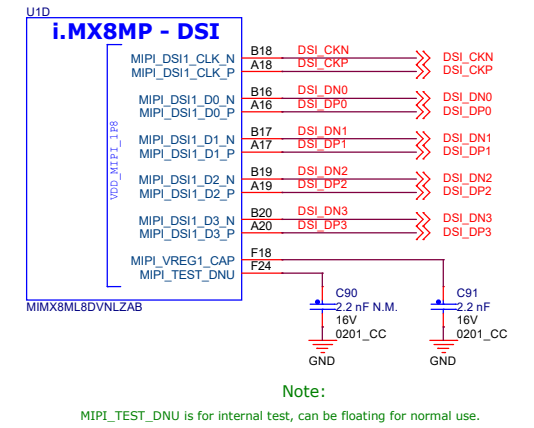
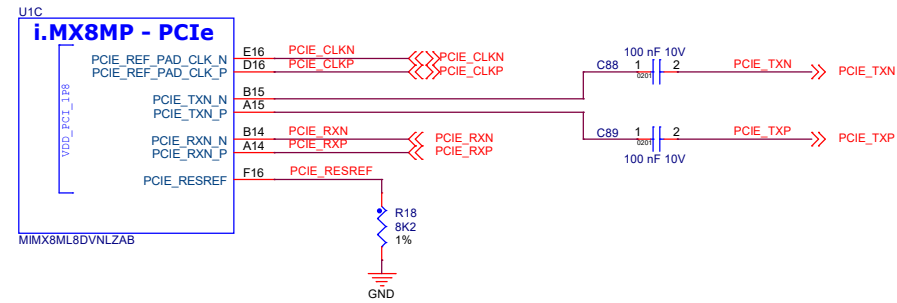
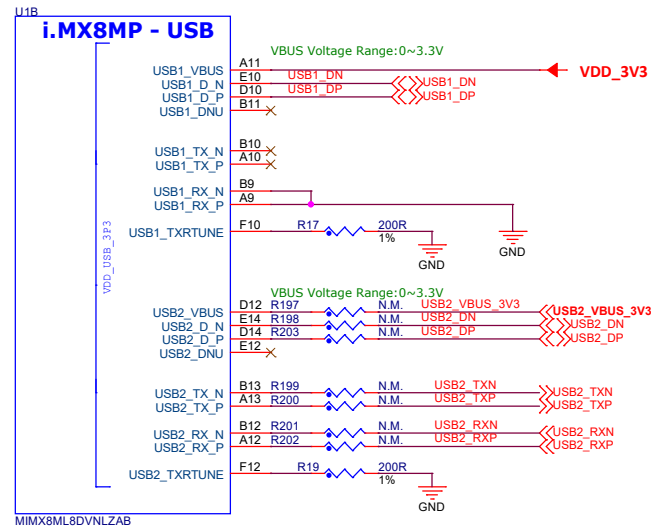
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Size A2		DWG NO	PCB03	Rev 3.0
09/09/2024				Sheet 6 of 17

# i.MX8M Plus PHYs



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<p>09/09/2024</p>	<p>Size</p>	<p>DWG NO <b>PCB03</b></p>
<p>Sheet 7 of 17</p>		<p>Rev 3.0</p>

# Boot Mode, CFG Switch and JTAG debug

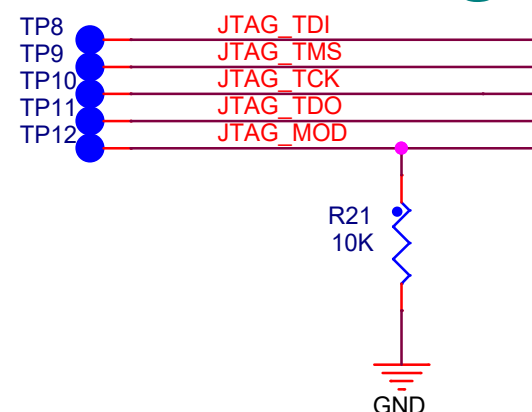
## Note

The only allowed configurations are the following:

### i.MX8M Plus Boot Mode

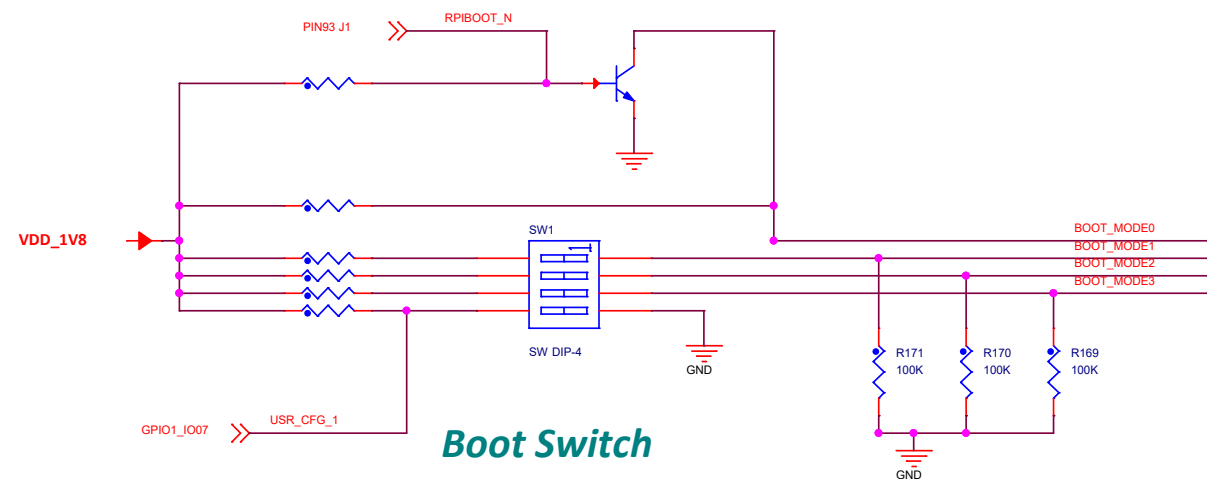
BOOT_MODE3	BOOT_MODE2	BOOT_MODE1	RPIBOOT_N (PIN93) DEFAULT=1	Boot Modes
0	0	0	0	Boot From Internal i.MX8M Fuses
0	0	0	1	USB Serial Download
0	0	1	0	USDHC3 (eMMC boot only, SD3 8-bit) Default
0	0	1	1	USDHC2 (SD boot only, SD2)
0	1	1	0	QSPI 3B Read
0	1	1	1	QSPI Hyperflash 3.3V
1	0	0	0	ecSPI Boot

## JTAG Debug



## Caution:


BOOT\_MODE0, BOOT\_MODE1, BOOT\_MODE2, BOOT\_MODE3, JTAG\_MOD and POR\_B must be pulled to "11111" for i.MX8M Plus to enter Boundary Scan mode.

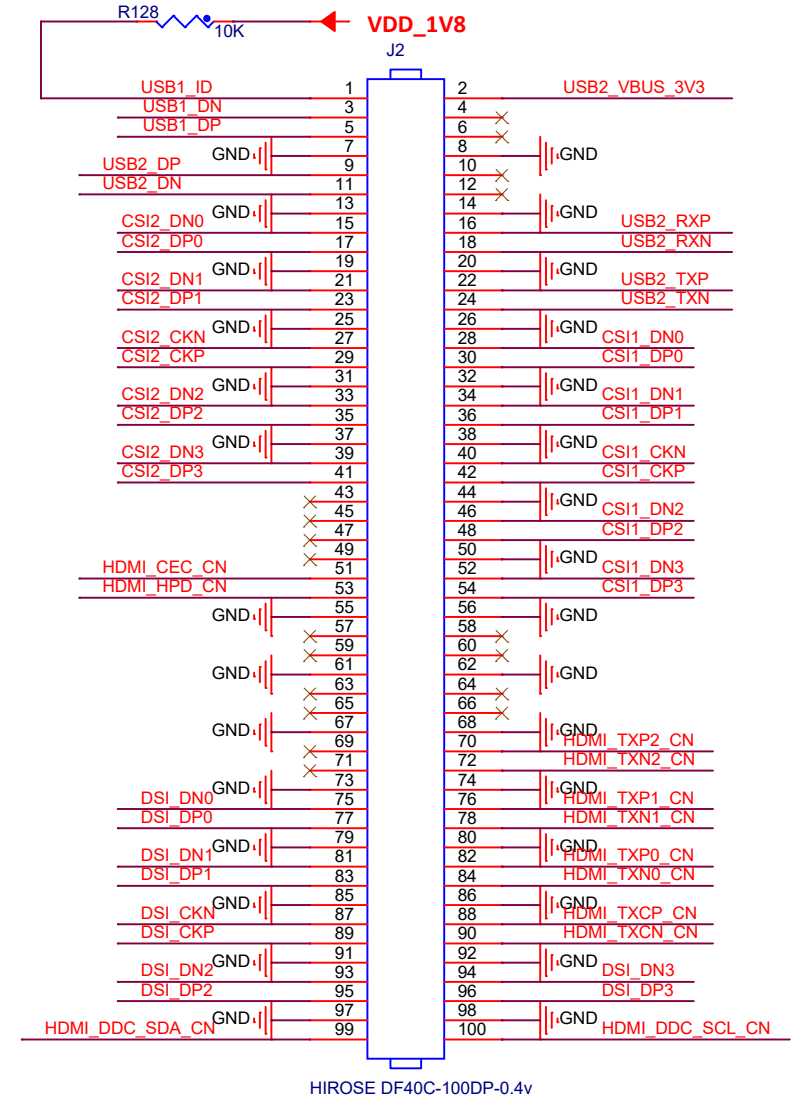
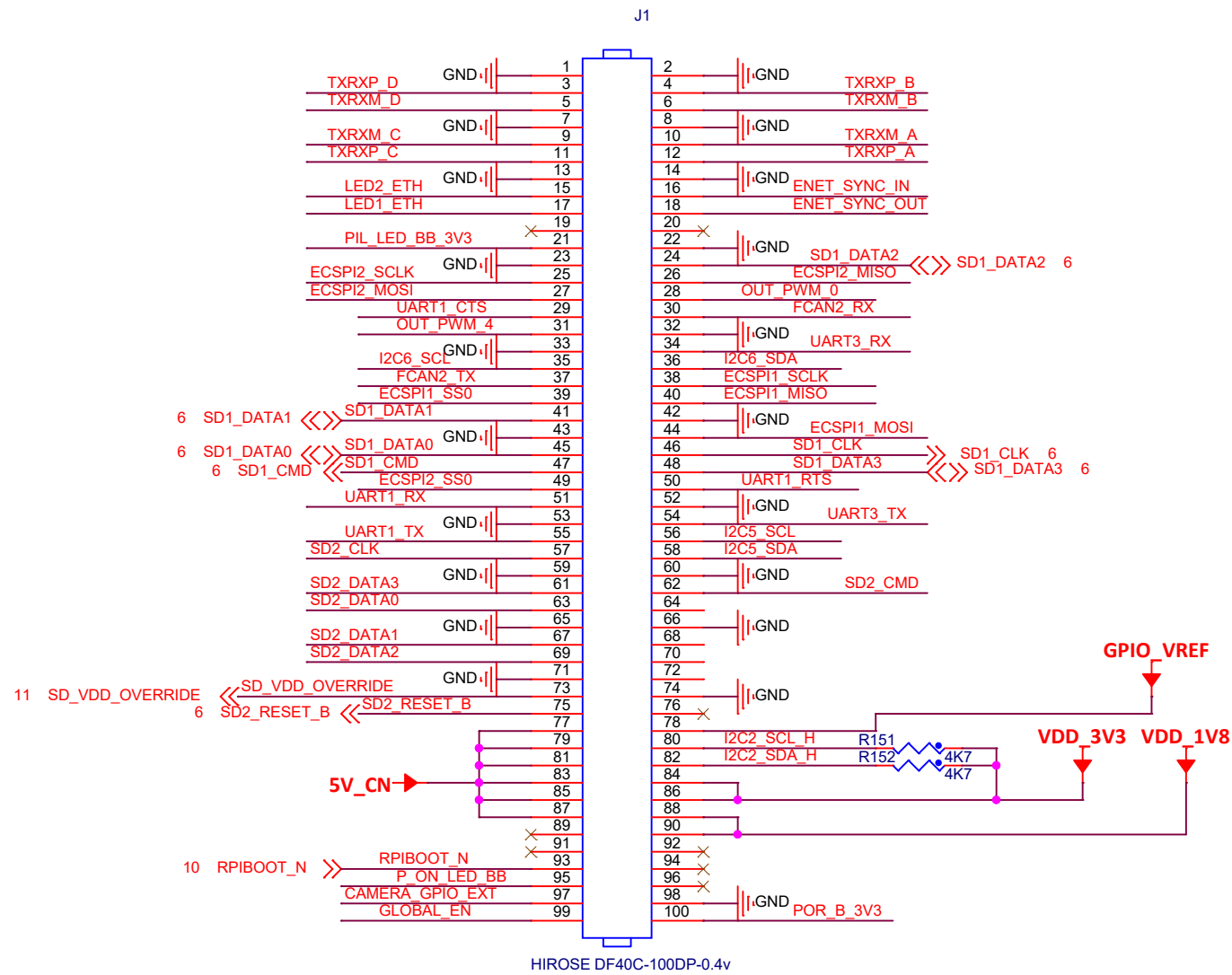



## Note

For i.MX8M Plus ROM fuse, please refer to NXP technical manual: IMX8MPRM.

RPIBOOT\_N is compliant to CM4 form factor carriers and is connected to BOOT\_MODE0.

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		<b>ASTRIAL v.1.0</b>	
Size A2	DWG NO PCB03	Rev 3.0	Sheet 10 of 17
09/09/2024			





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Size A3		DWG NO PCB03	Rev 3.0
09/09/2024		Sheet 13 of 17	